

Patent

**Attorney Docket No.: 2207/10121
Assignee: Intel Corporation**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Sailesh KOTTAPALLI et al.
SERIAL NO. : 09/751,762
FILED : December 29, 2000
FOR : METHOD FOR CONVERTING PIPELINE STALLS CAUSED
BY INSTRUCTIONS WITH LONG LATENCY MEMORY
ACCESSES TO PIPELINE FLUSHES IN A MULTITHREADED
PROCESSOR.
GROUP ART UNIT : 2183
EXAMINER : Daniel H. PAN

M/S: ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO EXAMINER'S INTERVIEW SUMMARY

Dear Sir:

The following remarks below are respectfully submitted in response to the Examiner's
Interview Summary dated April 24, 2008.

Remarks begin on page 2 of this paper.

REMARKS

First, Applicants would like to gratefully acknowledge the Examiner's consideration in conducting a telephone interview on April 24, 2008.

Claims 1-21 were discussed. Claims 1, 2, 9-11, 14-17, 20 and 21 are allowed. Claims 3-8, 12-13, and 18-19 have been cancelled without prejudice or disclaimer.

None of the prior art of record teaches the features for at least the reasons set forth in the Reasons for Allowance. In addition, as stated in the Examiner's Interview Summary, corrections to claim language formality in the Examiner's Amendment were done at the Examiner's request only for the sake of clarity, not for the purposes of overcoming prior art. Support for the allowed claims may be found at least at page 4, lines 1-22; page 5, lines 1-21; page 5, lines 22-23 and page 6, lines 1-22 of the specification.

The Examiner is invited to call the undersigned attorney at the telephone number listed below. The Office is hereby authorized to charge any fees, or credit any overpayments, to Deposit Account No. **11-0600**.

Respectfully submitted,

KENYON & KENYON LLP

Dated: June 5, 2008

By: /Sumit Bhattacharya/
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